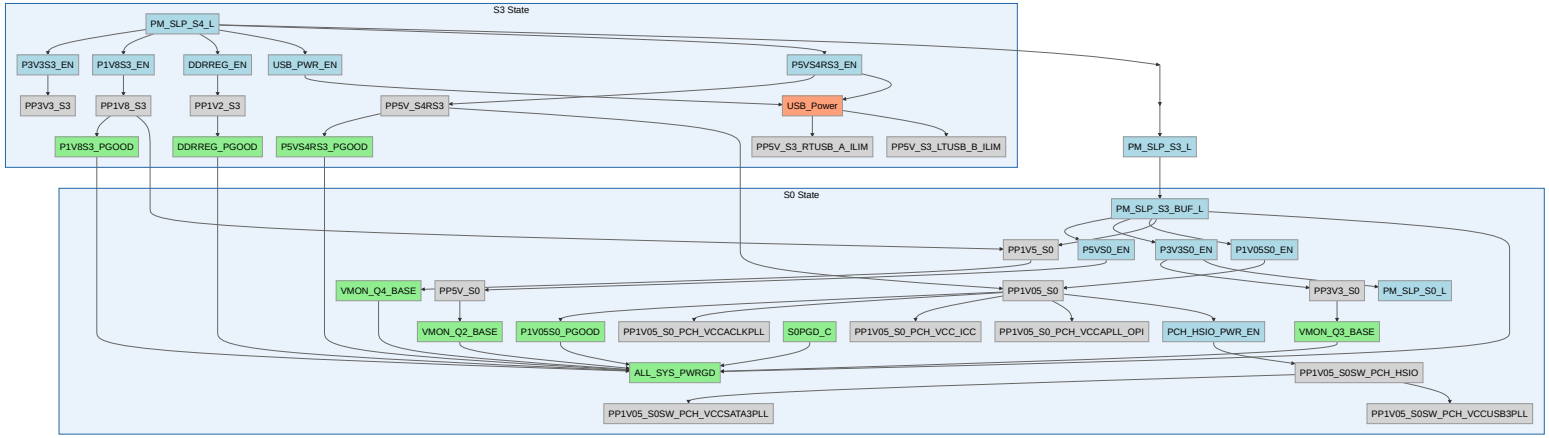
S3 State
PM_SLP_S4_L
P3V3S3_EN
P1V8S3_EN
DDRREG_EN
USB_PWR_EN
P5VS4RS3_EN
PP3V3_S3
PP1V8_S3
PP1V2_S3
PP5V_S4RS3
USB_Power
P1V8S3_PGOOD
DDRREG_PGOOD
P5VS4RS3_PGOOD
PP5V_S3_RTUSB_A_ILIM
PP5V_S3_LTUSB_B_ILIM
PM_SLP_S3_L
S0 State
PM_SLP_S3_BUF_L
PP1V5_S0
P5VS0_EN
P3V3S0_EN
P1V05S0_EN
VMON_Q4_BASE
PP5V_S0
PP1V05_S0
PP3V3_S0
PM_SLP_S0_L
VMON_Q2_BASE
P1V05S0_PGOOD
PP1V05_S0_PCH_VCCACLKPLL
S0PGD_C
PP1V05_S0_PCH_VCC_ICC
PP1V05_S0_PCH_VCCAPLL_OPI
PCH_HSIO_PWR_EN
VMON_Q3_BASE
ALL_SYS_PWRGD
PP1V05_S0SW_PCH_HSIO
PP1V05_S0SW_PCH_VCCSATA3PLL
PP1V05_S0SW_PCH_VCCUSB3PLL